

FIG 1

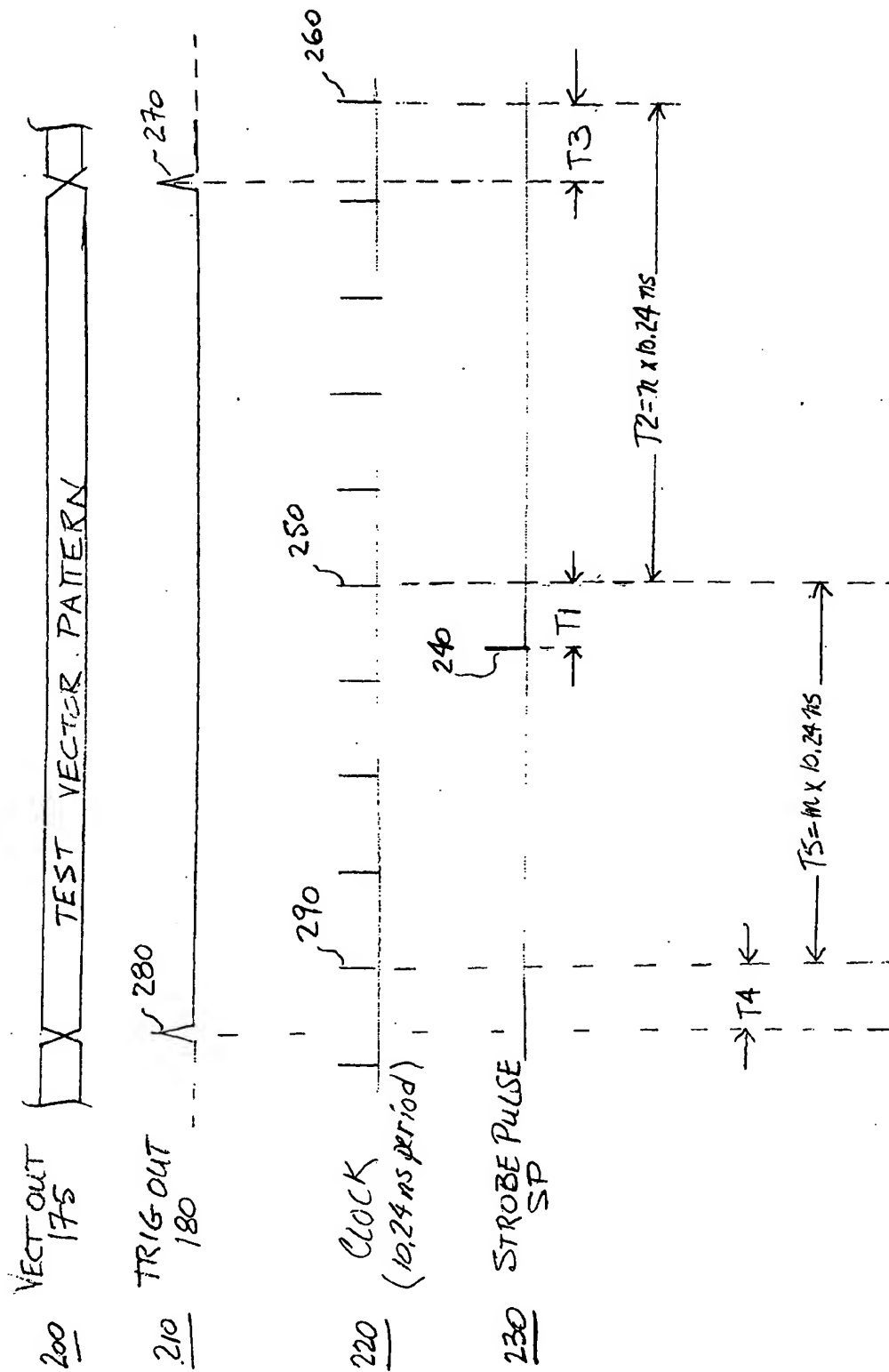


FIG 2

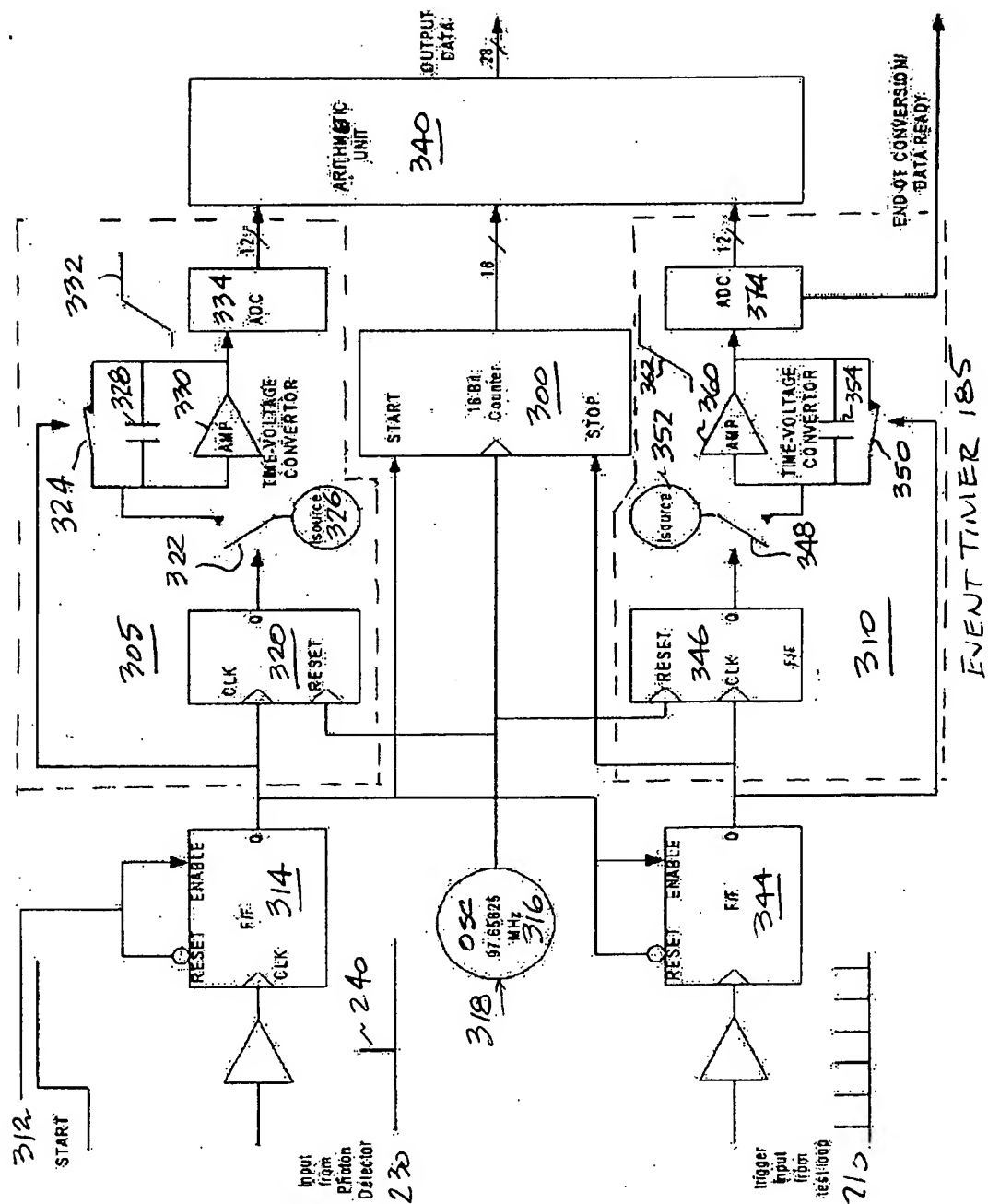


FIG. 3

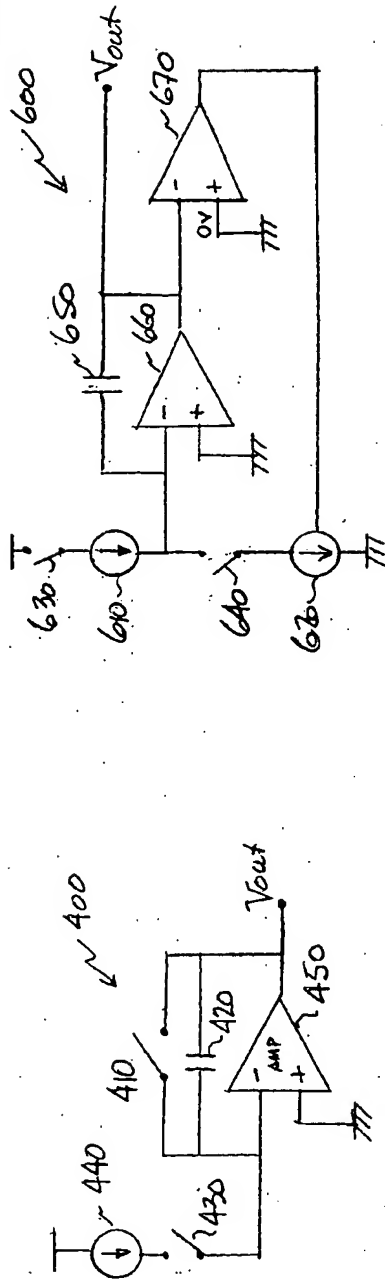
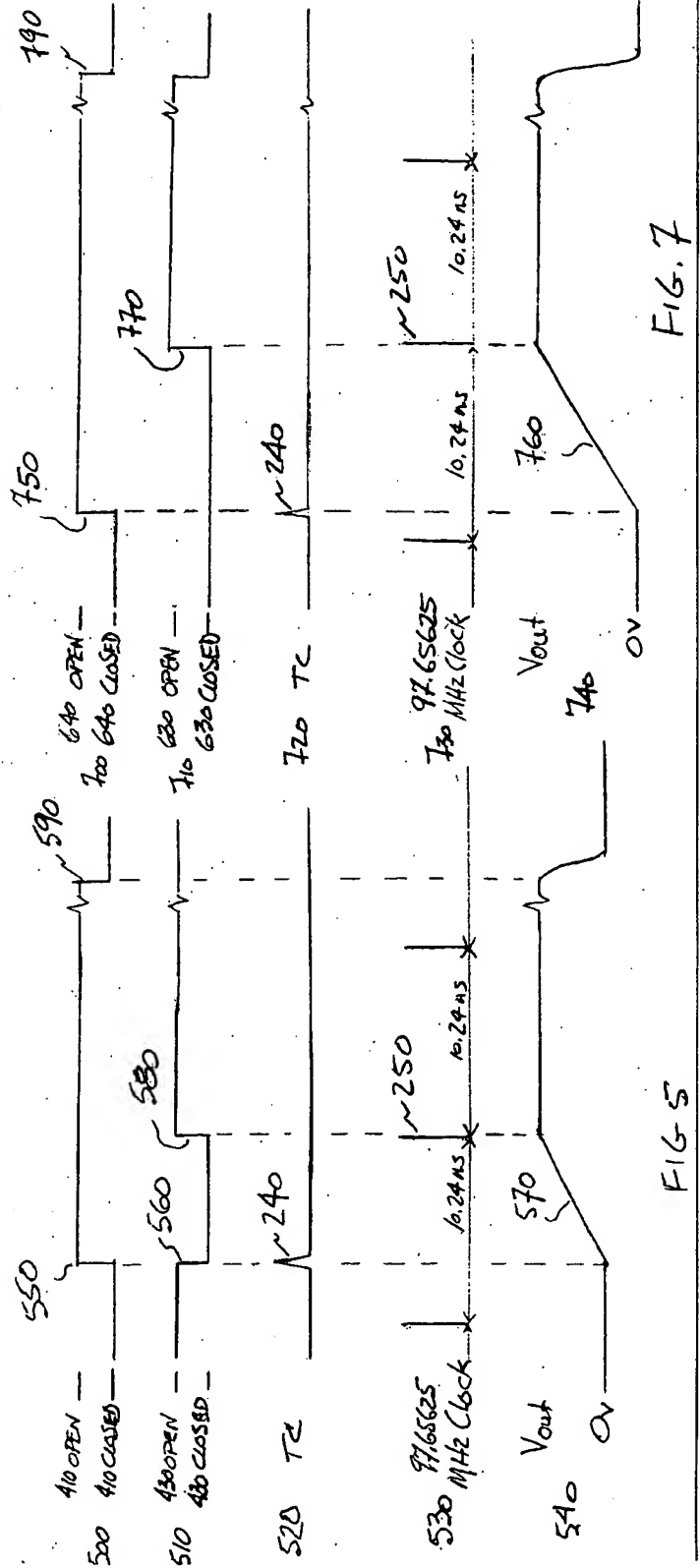
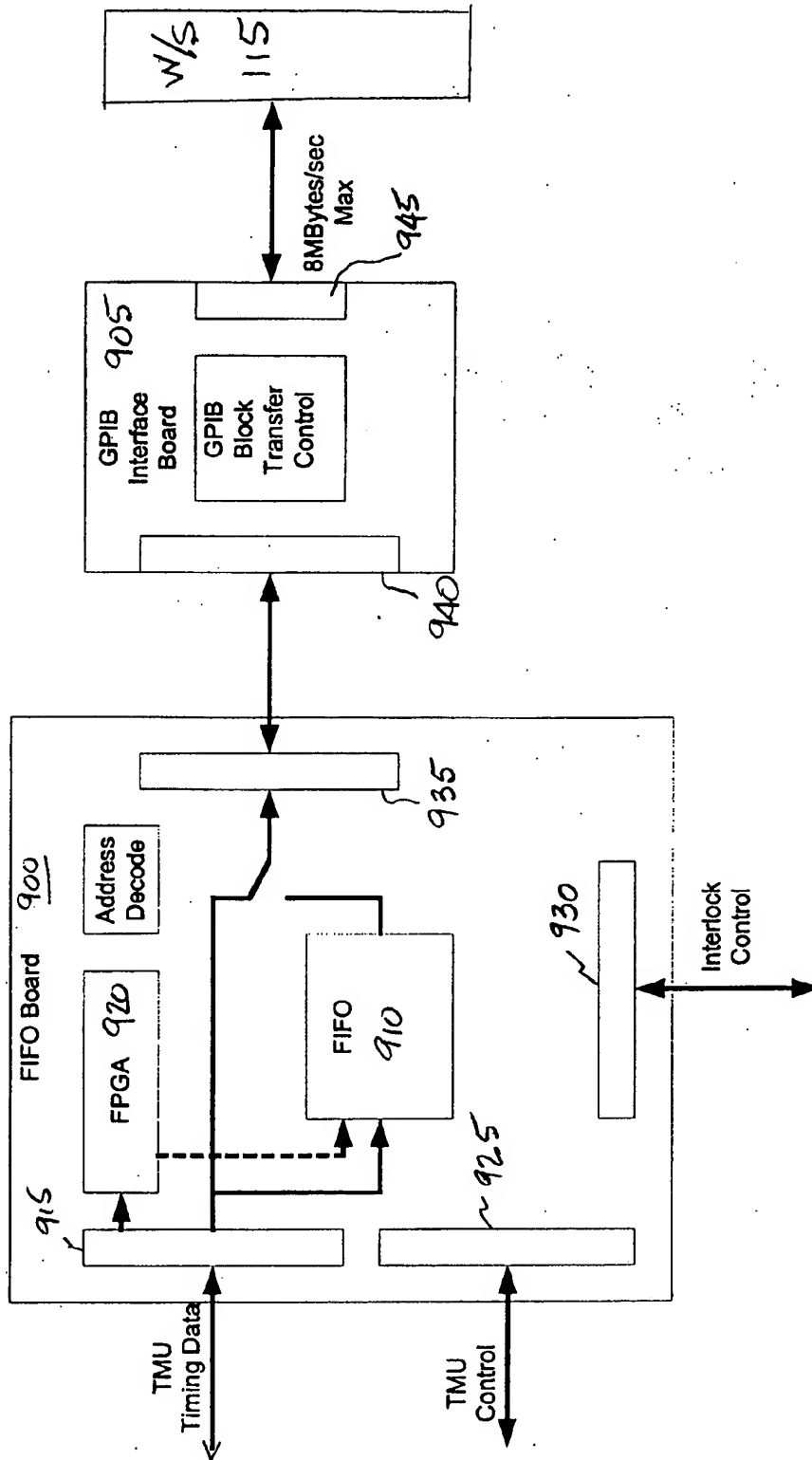


FIG 6

FIG 4





Data Path and System Control

FIG. 9

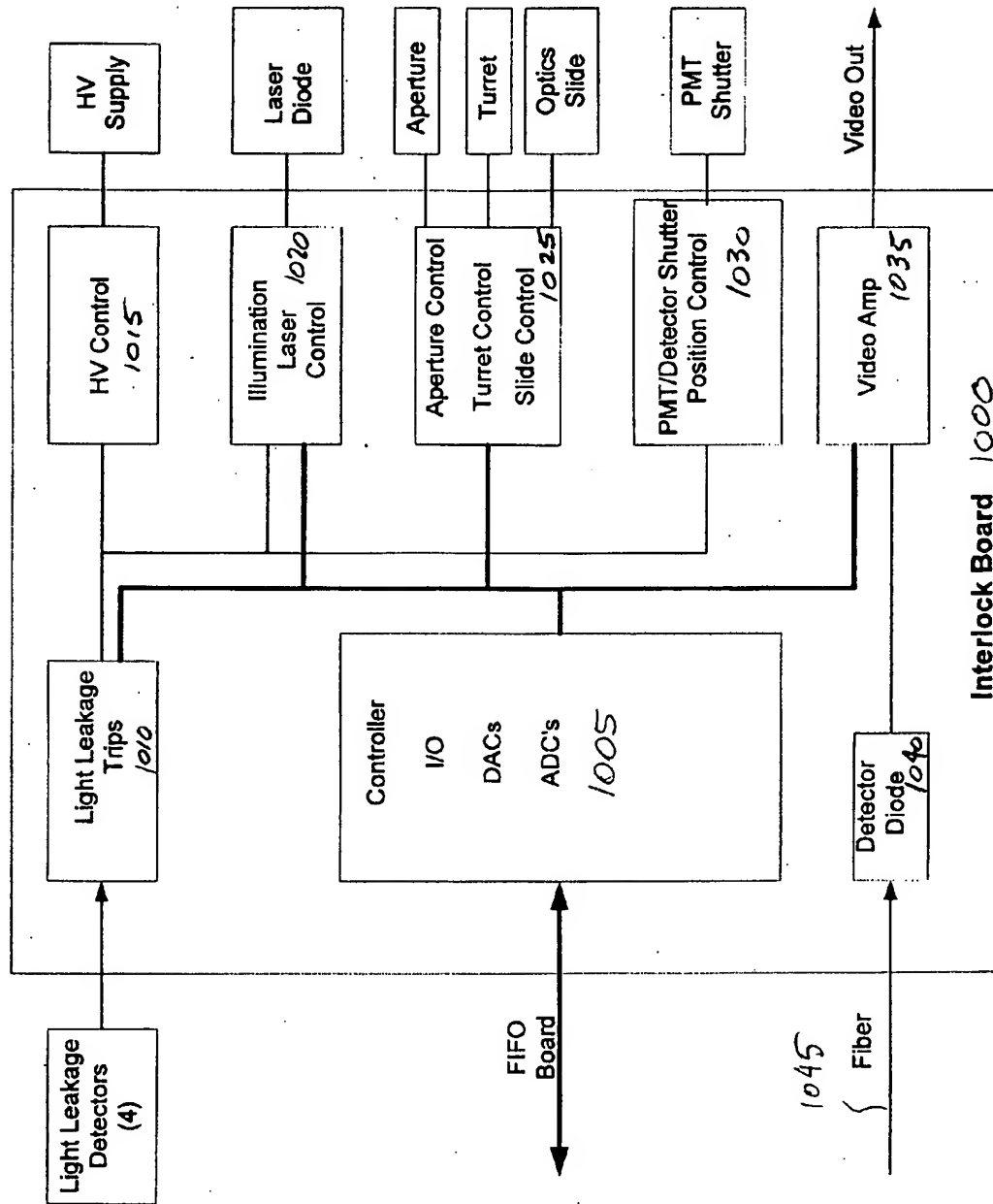
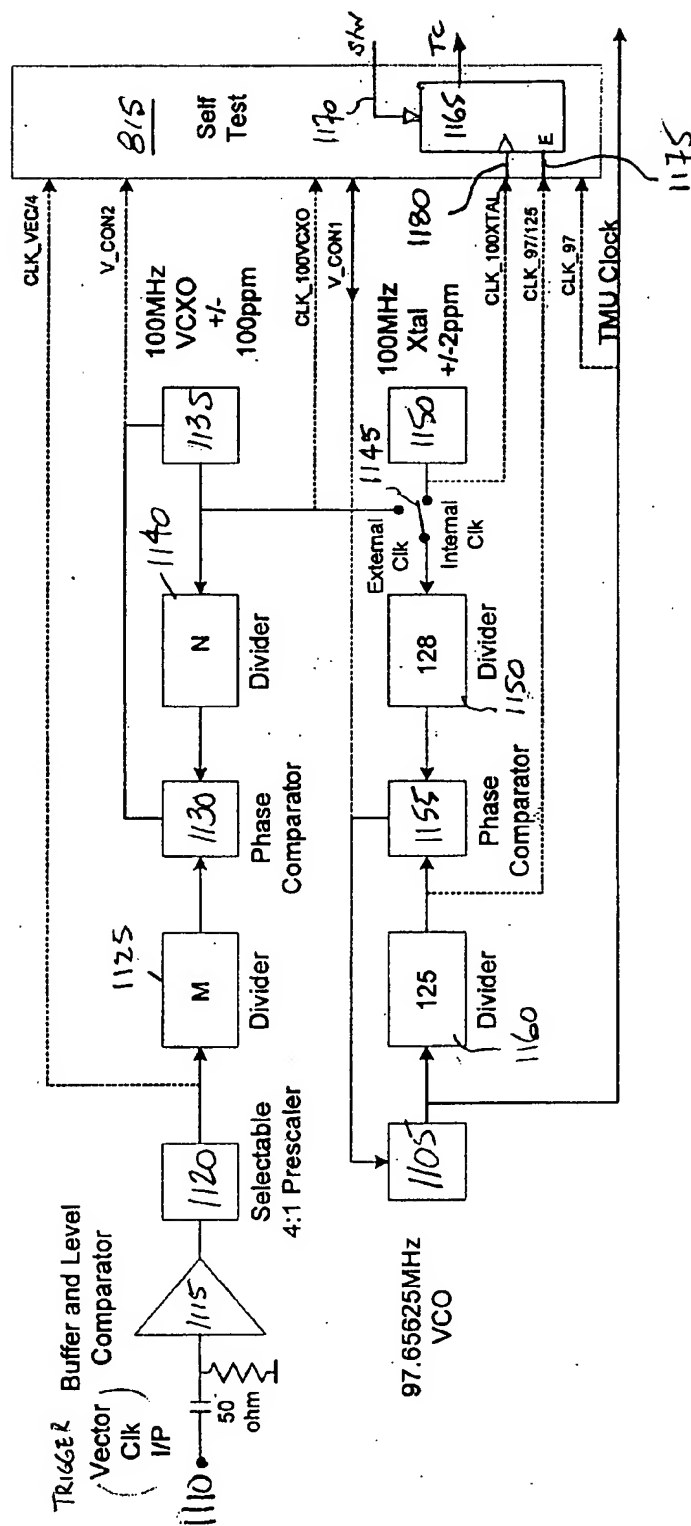


FIG. 10

TMU clock generator.

Fig 11



Interpolator Calibration Method

Counter LSB=1	Counter LSB=0
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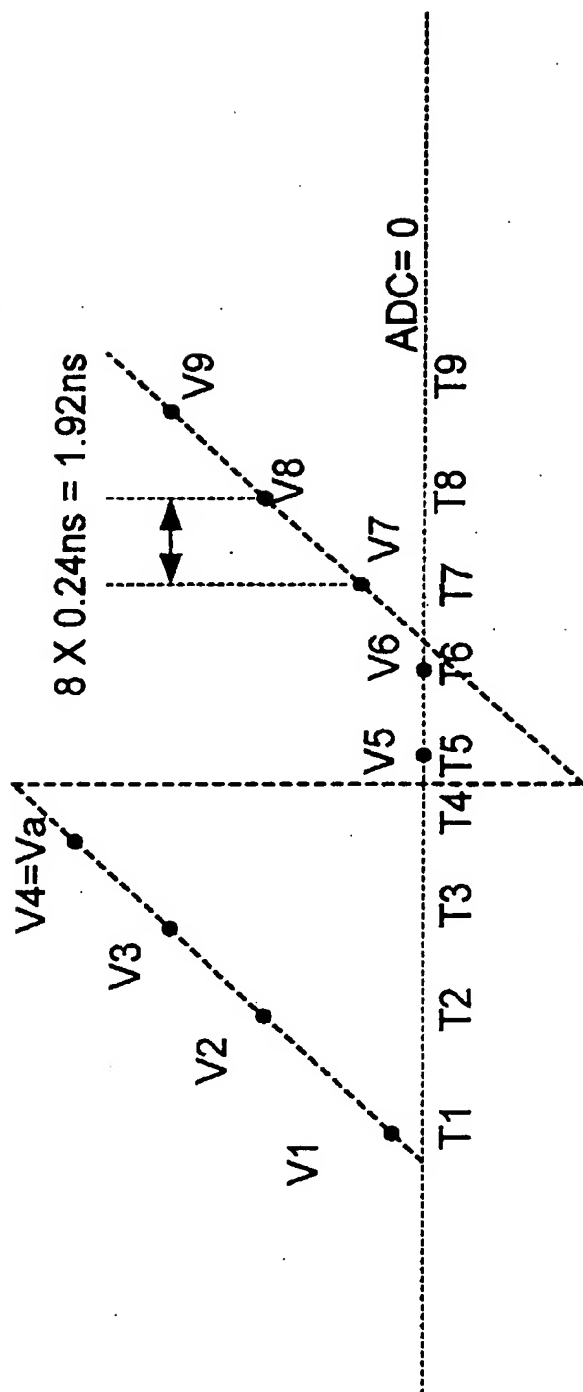


FIG. 12

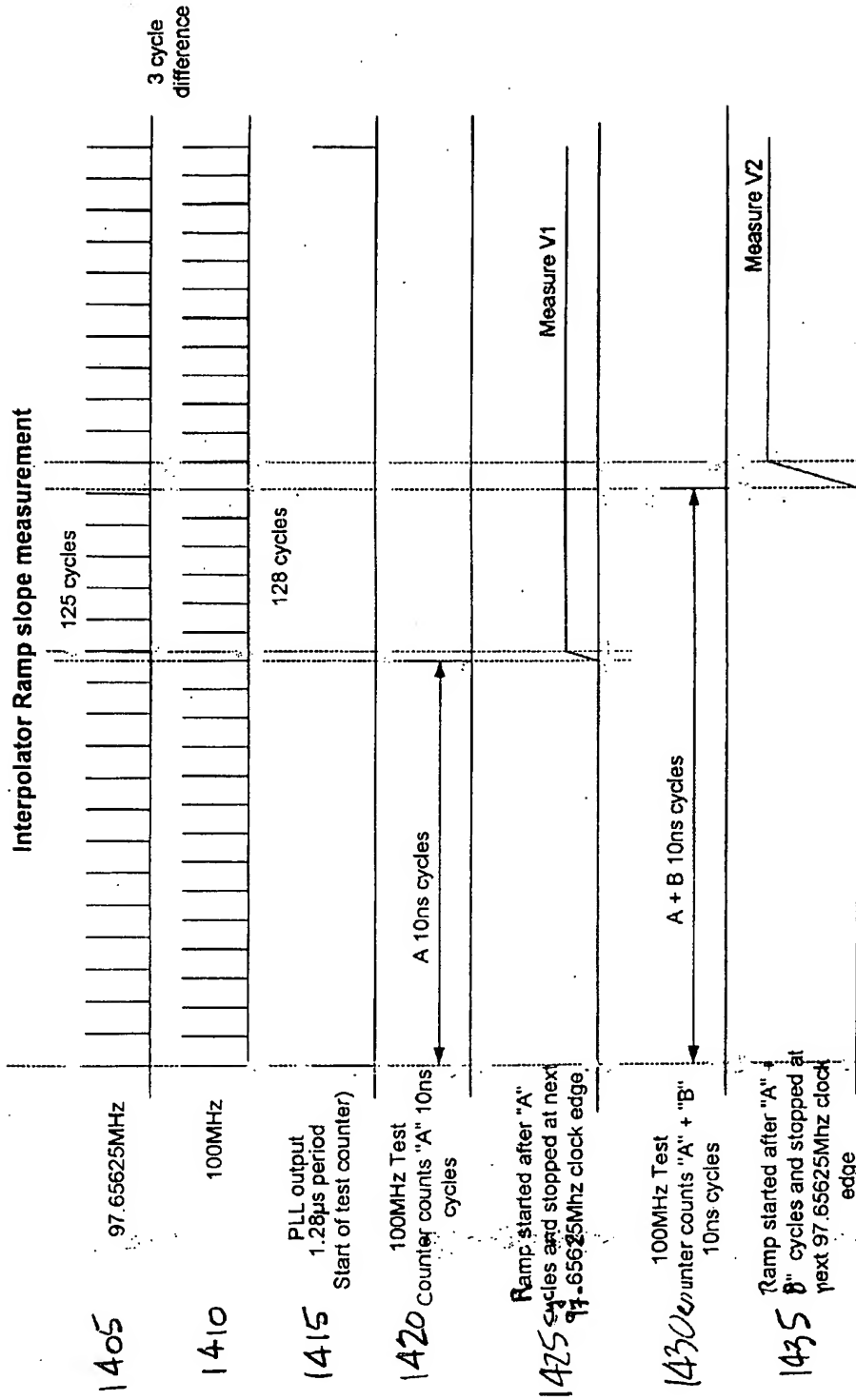


Fig. 14